#### **Features**

EXTREMELY Low Jitter
Low Cost
EXPRESS Delivery
Frequency Resolution to six decimal places
Stabilities to ±50 PPM
-20 to +70°C or -40 to +85°C operating temperatures
Tri-State Enable / Disable Feature
Industry Standard Package, Footprint & Pin-Out
Fully RoHS compliant
Gold over Nickel Termination Finish
Serial ID with Comprehensive Traceability

#### Picture of Part



## Description

The DEI XO3000-PF Crystal Oscillator is a breakthrough in configurable Frequency Control Solutions. It utilizes a family of proprietary ASICs, designed and developed by DEI, with a key focus on noise reduction technologies.

The 3<sup>rd</sup> order Delta Sigma Modulator reduces noise to the levels that are comparable to traditional Bulk Quartz and SAW oscillators. The ASICs family has ability to select the output type, input voltages, and temperature performance features.

With the express lead-time, low cost, low noise, wide frequency range, excellent ambient performance, it is an excellent choice over the conventional technologies.

Finished parts are 100% final tested.

#### **Applications**

ANY application requiring an oscillator SONET
Ethernet
Storage Area Network
Broadband Access
Microprocessors / DSP / FPGA
Industrial Controllers
Test and Measurement Equipment
Fiber Channel

### Model Selection Guide

 $GSXO1205 - \underbrace{H \ C}_{A} \ 3 \ 3 \ 5 \ \underbrace{R}_{A} - \underbrace{1 \ 0 \ 6}_{A} \ . \ \underbrace{2 \ 5}_{Besolutions \ to \ 6 \ places \ past \ the \ decimal \ point}^{Resolutions \ to \ 6 \ places}$ 

H = HCMOS C = Ceramic

O = Ouart=

3 = 3.2 x 2.5mm 5 = 5 x 3.2mm

 $7 = 7 \times 5$ mm

3 = 3.3 V  $0 = \pm 100 \text{ PPM}$ 2 = 2.5 V  $5 = \pm 50 \text{ PPM}$  blank = -20°C to +70°C R = -40°C to +85°C

2

L = LVDS Q = QuartzP = LVPECL

M = LVDS (pin 2 E/D)Q = LVPECL (pin 2 E/D)

X = HCMOS (comp 2<sup>nd</sup> Output)

### **Electrical Characteristics**

Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Frequency Range	Fo		0.750 to 250.000 MHz
Frequency Stability 1			100, 50 ppm
Temperature Range	T <sub>O</sub>	Standard operating Optional operating Storage	-20°C to +70°C -40°C to +85°C -55°C to +125°C
Supply Voltage	$V_{DD}$	Standard	3.3 V ± 5%
Input Current (@ 15pF LOAD)	I <sub>DD</sub>	0.75 ~ 20 MHz 20+ ~ 50 MHz 50+ ~ 130 MHz 130+ ~ 200 MHz 200+ ~ 250 MHz	32 mA 35 mA 47 mA 55 mA 60 mA
Output Load	HCMOS	Standard Operational To 125MHz	15 pF 30 pF
Start-Up Time	Ts		10 mS
Output Enable / Disable Time			100 nS
Moisture Sensitivity Level	MSL	JEDEC J-STD-20	1
Termination Finish			Au

Note 1 – Stability is inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock and vibration.

# Absolute Maximum Ratings (Useful life may be impaired. For user guidelines only, not tested)

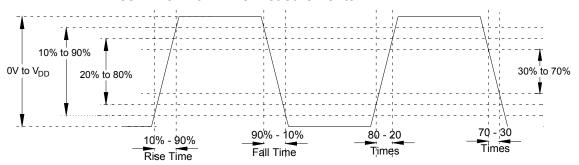
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Input Voltage	$V_{DD}$		-0.5V to +5.0V
Operating Temperature	T <sub>AMAX</sub>		–55°C to +105°C
Storage Temperature	T <sub>STG</sub>		–55°C to +125°C
Junction Temperature			150°C
ESD Sensitivity	HBM	Human Body Model	>1 kV

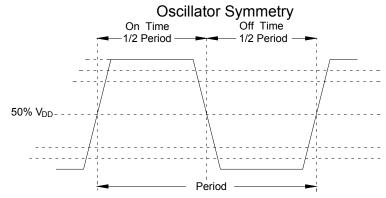
# **Output Wave Characteristics**

Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Output LOW Voltage	V <sub>OL</sub>	0.75 to 150 MHz 150+ to 250 MHz	10% V <sub>DD</sub> 20% V <sub>DD</sub>
Output HIGH Voltage	V <sub>OH</sub>	0.75 to 150 MHz 150+ to 250 MHz	90% V <sub>DD</sub> MIN 80% V <sub>DD</sub> MIN
Output Symmetry (See Drawing Below)		@ 50% V <sub>DD</sub> Level	45% ~ 55%
Output Enable (PIN # 1) Voltage	V <sub>H</sub>		≥70% V <sub>DD</sub>
Output Disable (PIN # 1) Voltage	V <sub>IL</sub>		≤ 30% V <sub>DD</sub>
Cycle Rise Time (See Drawing Below)	T <sub>R</sub>	0.75 to 150 MHz 150+ to 250 MHz	3 nS <sub>(10%~90%)</sub> 3 nS <sub>(20%~80%)</sub>
Cycle Fall Time (See Drawing Below)	T <sub>F</sub>	0.75 to 150 MHz 150+ to 250 MHz	3 nS <sub>(90%~10%)</sub> 3 nS <sub>(80%~20%)</sub>

If 30% to 70% times are used, Rise and Fall times change to 1.5 nS from 0.75 to 250MHz If 20% to 80% times are used, Rise and Fall times change to 2 nS from 0.75 to 150MHz

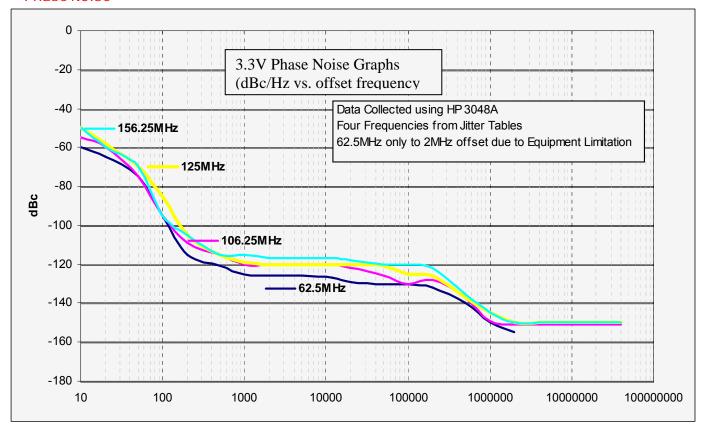
#### Rise Time / Fall Time Measurements





Ideally, Symmetry should be 50/50 -- Other expressions are 45/55 or 55/45

#### Phase Noise



Jitter is frequency dependent. Below are typical values at select frequencies.

#### Phase Jitter & Time Interval Error (TIE)

Frequency	Phase Jitter (12kHz to 20MHz)	TIE (Sigma of Jitter Distribution)	Units
62.5 MHz	0.8	2.9	pS RMS
106.25 MHz	0.8	3.2	pS RMS
125 MHz	0.8	2.3	pS RMS
156.25 MHz	1.0	3.0	pS RMS

**Phase Jitter** is integrated from HP3048 Phase Noise Measurement System; measured directly into 50 ohm input;  $V_{DD} = 3.3V$ . **TIE** was measured on LeCroy LC684 Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software;  $V_{DD} = 3.3V$ . *Per MJSQ* spec (Methodologies for Jitter and Signal Quality specifications)

# Random & Deterministic Jitter Composition

Frequency	Random (Rj) (pS RMS)	Deterministic (Dj)	Total Jitter (Tj) (14 x Rj) + Dj
62.5 MHz	1.2	9.2	25.6 pS
106.25 MHz	1.3	9.0	27.2 pS
125 MHz	1.2	8.7	26.5 pS
156.25 MHz	1.2	12.7	30.0 pS

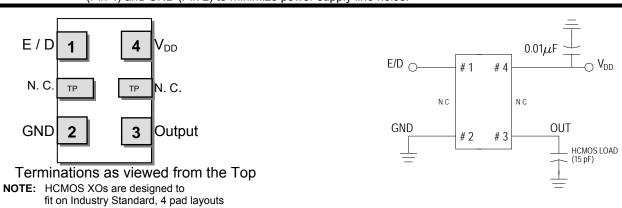
**Rj and Dj**, measured on LeCroy LC684 Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software. Per **MJSQ** spec (Methodologies for Jitter and Signal Quality specifications)

# Pin Description and Recommended Circuit

Pin#	Name	Туре	Function
1	E/D <sup>1</sup>	Logic	Enable / Disable Control of Output (0 = Disabled)
2	GND	Ground	Electrical Ground for V <sub>DD</sub>
3	Output	Output	HCMOS Oscillator Output
4	V <sub>DD</sub> 2	Power	Power Supply Source Voltage
Test Points	N. C.	Hi Z	No Connection (Factory Use ONLY)

#### **NOTES:**

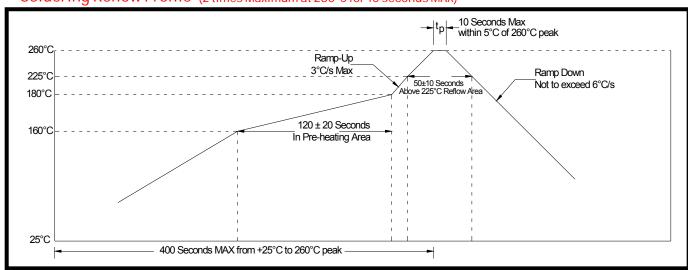
- Includes pull-up resistor to  $V_{\text{DD}}$  to provide output when the pin (1) is No Connect.
- Installation should include a 0.01μF bypass capacitor placed between V<sub>DD</sub> (Pin 4) and GND (Pin 2) to minimize power supply line noise.



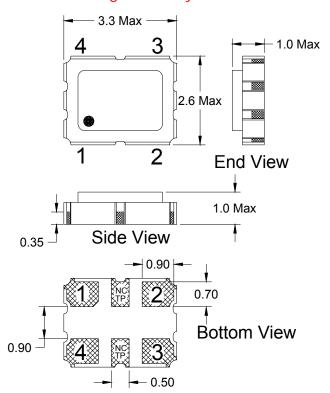
#### Enable / Disable Control

Pin # 1 (state)	Output (Pin # 3)
OPEN (No Connection)	ACTIVE Output
"1" Level V <sub>IH</sub> ≥ 70% V <sub>DD</sub>	ACTIVE Output
"0" Level $V_L \le 30\% V_{DD}$	High Impedance

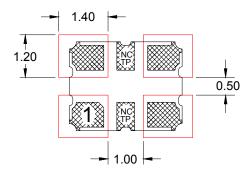
# Soldering Reflow Profile (2 times Maximum at 260°C for 10 seconds MAX)



# Mechanical Dimensional Drawing & Pad Layout



# Recommended Solder Pad Layout



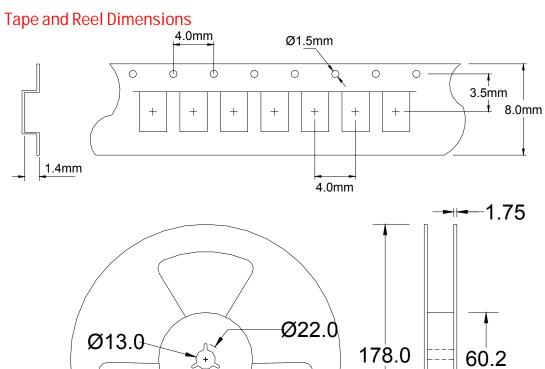
Note: HCMOS XO's are designed to fit on industry standard, 4 pad layouts.

#### **Pin Connections**

#1 E/D #3 V<sub>OUT</sub> #2 GND #4 V<sub>DD</sub>

Drawing is for reference to critical specifications defined by size measurements. Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary

9.0



# **Mechanical Testing**

Parameter	Test Method
Mechanical Shock	Drop from 75cm to hardwood surface – 3 times
Mechanical Vibration	10~55Hz, 1.5mm amplitude, 1 Minute Sweep 2 Hours each in 3 Directions (X, Y, Z)
High Temperature Burn-in	Under Power @ 125°C for 2000 Hours (results below)
Hermetic Seal	He pressure: 4 ±1 kgf / cm <sup>2</sup> 2 Hour soak

# 2,000 Hour Burn-In

Burn-In Testing – under power 2000 Hours, 125°C

