### **Features**

40 to 200 MHz
7.0 mm x 5.0 mm x 1.8 mm ceramic SMD
Compact and lightweight
Differential LVDS Outputs
2.5 or 3.3 v supply
0.3 ps integrated phase jitter typ.

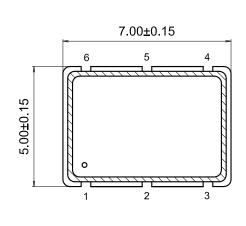
### **Picture of Part**

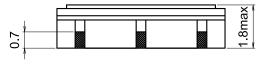


### **Description**

The GSXO1201 features the use of high frequency fundamental crystals in non-PLL based circuitry to achieve the lowest possible jitter and phase noise performance. LVDS outputs exceed the requirements for SONET, XDSL, and other telecommunication standards.

### **Physical Dimensions & Pin Connections**

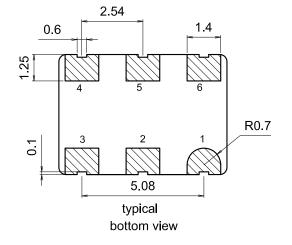


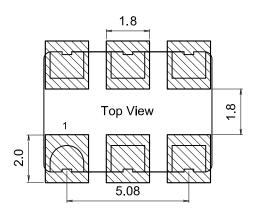


Pad1	Tri-state	Pad4	LVDS Output
Pad2	No Connection	Pad5	LVDS Output
Pad3	Ground	Pad6	Supply Voltage

Rounded pad is pad No.1







# **Specification**

LVDS XO ( Next Generation ) Frequency Range		Sym. Condition	Value			Unit	Note			
				Min.	Typ.	Max.	<u> </u>			
		$f_0$		40		200	MHz			
	Output High				1 420	1 600				
	Voltage				1.430	1.600	V			
LVDS Outputs	Output Low Voltage			0.900	1.100		V			
	Voltage									
	Output Differential		Voltage	250	350	450	MV	millivolts		
	Duty Cycle		Measured at 50% of waveform	45	50	55	%			
	Rise / Fall Time				0.20	0.40	ns	**measured at 20 to 80% of the		
						- 10		waveform		
			Start up Time		3.0	10	mS			
<u> </u>	,									
Power supp Voltage	DIY	Vcc		3 150	3.300	3.450	V	2.5V +/- 5% available		
Current con	sumption	Icc	Current Drain is a	3 130				2.3 v T/- 3/0 available		
		100	function of frequency		16.0	27.0	mA			
Pad 1 Enab	le Disable Function									
			Voltage applied to pad 1	70% Vcc			volts	In the disabled mode, both output are enabled when Pad 1 is taken		
								Above 70% of Vcc ref to		
								Ground (threshold)		
Pad 1 Enab	le Disable Function									
			Voltage applied to pad 1			30% Vcc	volts	Both outputs are disabled when		
			Oscillator circuit is always on. Only buffer circuit is turned OFF					Pad 1 is taken below 30% Vcc.		
			_	1KHz	10KHz	1001/11-				
			Freq. OFFSET	IKHZ	TUKHZ	100KHz				
Typical Pha	se Noise as a									
Function of	Operating Frequency		125 MHz	-120	-136	-142				
								4		
	<u> </u>		Frequency Domain		0.300		PS RMS	Integrated from 12KHz to 20MHz		
			Phase Jitter		0.300		ES KIVIS	Max.		
Environme	ntal, mechanical cond	litions.	ı	1			l	1		
	emperature range		-40°C to +85°C maximum rang	e available	that is stan	dard				
Storage temperature range		-55°C to 125°C								
Humidity Vibration		85% RH; 85C; 48 hours of expo	85% RH; 85C; 48 hours of exposure							
Shock		Mil-Std 202F, Method 204, 35G's, 50 to 2000 Hz Mil-Std 202F, Method 213B, test condition E, 1000 GG half sine wave								
Reflow			+260°C for 10 seconds							

XO Specification		Sym. Condition	Value		Unit	Note		
				Min.	Typ.	Max.		
Frequency S	tability vs. Temp.	$f_0$						
					1			
F	Commercial		-40°C to +85°C, ref 25°C	-25.0		+25.0	PPM	**Best Stability available
Frequency Versus			OR	-50.0		+50.0	PPM	
Operating Temperature	Industrial		-10°C to +70°C, ref 25°C	-100.0		+100.0	PPM	
Frequency Versus			Frequency versus Time PER YR	-3.0		+3.0	PPM	
Versus Time								

### **Ordering Information**

#### GSXO1201-XXX.XXXXXXX-W-Y

- 1. Field "XXX.XXXXXX " is the Output Frequency to six decimals in MHz
- 2. Field "W" is Operating Temperature Range and Freq. Stability:
  - a. "0" for  $-10^{\circ}$ C to  $+70^{\circ}$ C and +/-25 ppm
  - b. "1" for -10°C to +70°C and +/- 50 ppm
  - c. "2" for  $-10^{\circ}$ C to  $+70^{\circ}$ C and +/-100 ppm
  - d. "3" for -40°C to +85°C and +/- 25 ppm
  - e. "4" for -40°C to +85°C and +/- 50 ppm
  - f. "5" for -40°C to +85°C and +/- 100 ppm
- 3. Field "Y" is Power Supply Option
  - a. "0" for 2.5 V +/- 5%
  - b. "1" for 3.3 V +/- 5%

### **Part Number Example**

GSXO1201-125.000000-5-0

125.000000 MHz Operating Frequency

Operating Temperature of -40°C to +85°C

+/- 100 ppm Overall Frequency Stability

2.5 V +/- 5% supply voltage

## **Typical Phase Noise Plot**

