GSVX1203
VCXO

Features

EXTREMELY Low Jitter
Low Cost
EXPRESS Delivery
Frequency Resolution to six decimal places
Absolute Pull Range (APR) of ±50ppm
-20 to +70°C or -40 to +85°C operating temperatures
Tri-State Enable / Disable Feature
Industry Standard Package, Footprint & Pin-Out
Fully RoHS compliant
Gold over Nickel Termination Finish
Serial ID with Comprehensive Traceability

Picture of Part

Description

The GSVX1203 Crystal Oscillator is a breakthrough in configurable Frequency Control Solutions. It utilizes a family of proprietary ASICs, designed and developed, with a key focus on noise reduction technologies.

The 3rd order Delta Sigma Modulator reduces noise to the levels that are comparable to traditional Bulk Quartz and SAW oscillators. The ASICs family has ability to select the output type, input voltages, and temperature performance features.

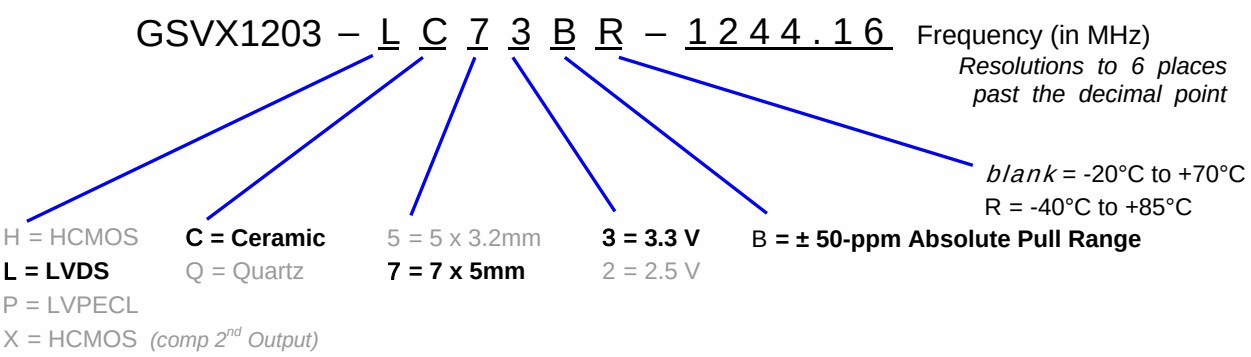
With the express lead-time, low cost, low noise, wide frequency range, excellent ambient performance, it is an excellent choice over the conventional technologies.

Finished parts are 100% final tested.

Applications

ANY application requiring an oscillator
SONET
Ethernet
Storage Area Network
Broadband Access
Microprocessors / DSP / FPGA
Industrial Controllers
Test and Measurement Equipment
Fiber Channel

Model Selection Guide



Absolute Maximum Ratings (Useful life may be impaired. For user guidelines only, not tested)

Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Input Voltage	V_{DD}		–0.5V to +5.0V
Operating Temperature	T_{AMAX}		–55°C to +105°C
Storage Temperature	T_{STG}		–55°C to +125°C
Junction Temperature			150°C
ESD Sensitivity	HBM	Human Body Model	1 kV

Electrical Characteristics

Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Frequency Range	F_O		0.750 MHz to 1.35 GHz
Absolute Pull Range Note 1	APR		± 50 ppm MIN
Temperature Range	T_O T_{STG}	Standard operating *Optional operating* Storage	-20°C to +70°C *-40°C to +85°C* -55°C to +125°C
Supply Voltage	V_{DD}	Standard	3.3 V ± 5%
Input Current (@ 100 Ohm LOAD)	I_{DD}	Standard Load	100 mA
Output Load		Standard	100 Ohms Typ.
Start-Up Time	T_S		10 mS
Output Enable / Disable Time			100 nS
Moisture Sensitivity Level	MSL	*JEDEC J-STD-20*	1
Termination Finish			Au

Note 1 – Stability is inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock and vibration.

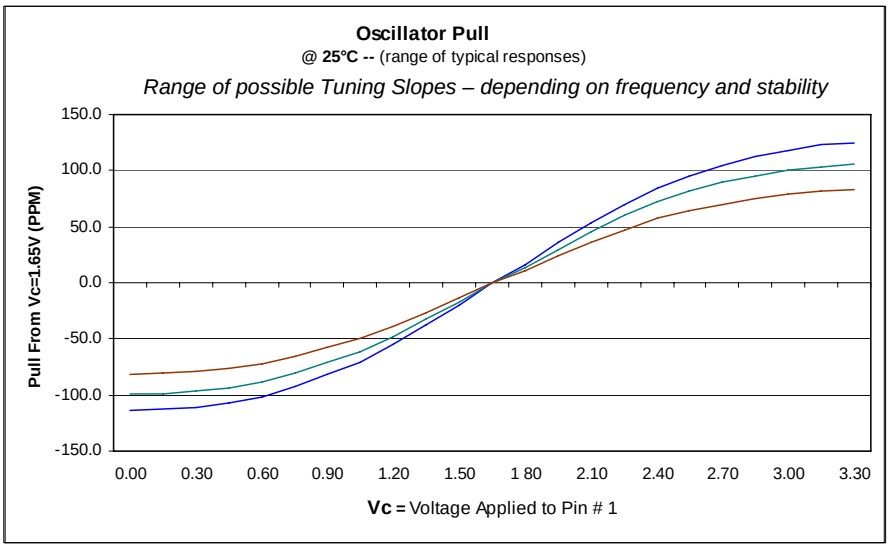
Frequency Control (Vc) Input -- pin # 1

Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Control Voltage Tuning Slope [1]		0V to V_{DD}	40 ~ 75 ppm/V Typ[2]
Control Voltage Linearity [2]	L_{VC}		± 10%
Control Voltage Tuning Range	V_C		0V ~ 3.3V
Modulation Bandwidth	BW		10 kHz Min
Nominal Control Voltage	V_{CNOM}	@ f_0	1.65V

NOTES:

[2] Actual slope is affected by frequency and accuracy settings.

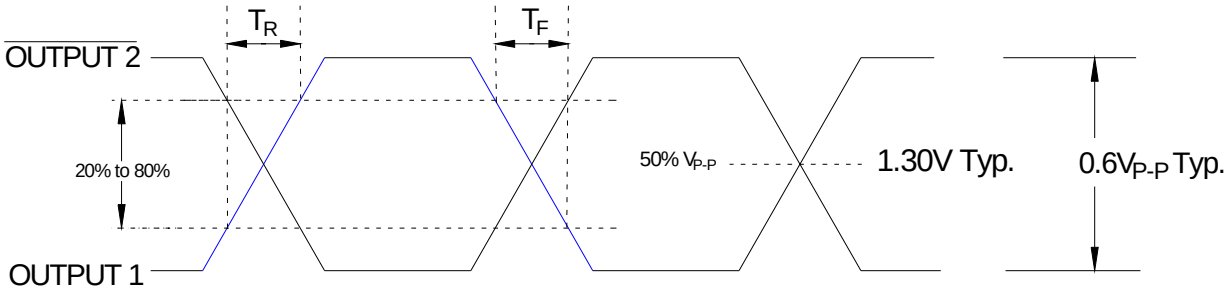
[3] For an example of linearity, see the graph below. *(The middle line represents the default factory setting)*



Output Wave Characteristics

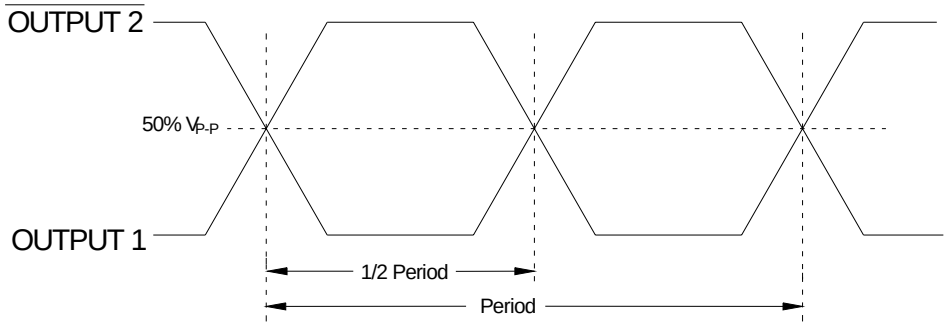
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Differential Output Voltage	V_{OD}	0.75 MHz to 1.35 GHz	0.6V Typ.
Output Offset Voltage	V_{OS}		1.3V Typ.
Output Symmetry (See Drawing Below)		@ 50% V_{P-P} Level	45% ~ 55%
Output Enable (PIN # 2) Voltage	V_H		> 70% V_{DD}
Output Disable (PIN # 2) Voltage	V_{IL}		< 30% V_{DD}
Cycle Rise Time (See Drawing Below)	T_R	0.75 MHz to 1.35 GHz	400 pS (20%~80%)
Cycle Fall Time (See Drawing Below)	T_F	0.75 MHz to 1.35 GHz	400 pS (80%~20%)

Rise Time / Fall Time Measurements

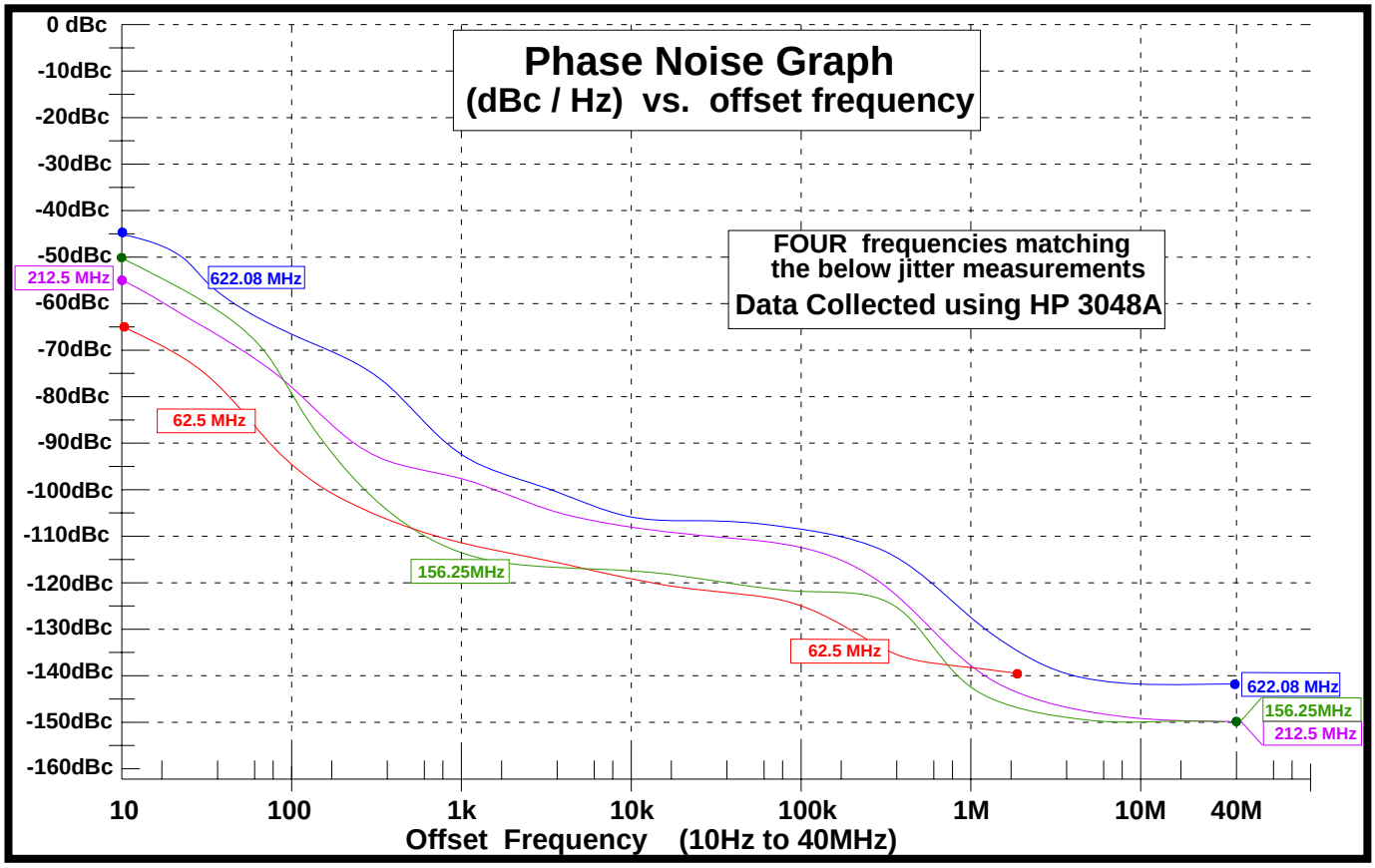


Oscillator Symmetry

Ideally, Symmetry should be 50/50 for 1/2 period -- Other expressions are 45/55 or 55/45



Phase Noise



Jitter is frequency dependent. Below are typical values at select frequencies.

LVDS Phase Jitter & Time Interval Error (TIE)

Frequency	Phase Jitter (12kHz to 20MHz)	T I E (Sigma of Jitter Distribution)	Units
62.5 MHz	0.77	3.0	pS RMS
156.25 MHz	1.19	3.6	pS RMS
212.5 MHz	0.89	3.9	pS RMS
622.08MHz	0.99	3.2	pS RMS

Phase Jitter is integrated from HP3048 Phase Noise Measurement System; measured directly into 50 ohm input; V_{DD} = 3.3V.
TIE was measured on LeCroy LC684 Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software; V_{DD} = 3.3V.
*Per **MJSQ** spec (Methodologies for Jitter and Signal Quality specifications)*

LVDS Random & Deterministic Jitter Composition

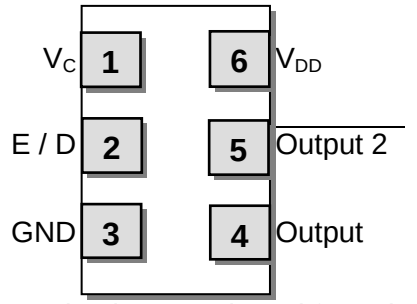
Frequency	Random (Rj) (pS RMS)	Deterministic (Dj) (pS P-P)	Total Jitter (Tj) (14 x Rj) + Dj
62.5 MHz	1.3	7.0	24.9 pS
156.25 MHz	1.3	5.8	23.6 pS
212.5 MHz	0.9	6.7	18.7 pS
622.08 MHz	1.1	5.3	20.7 pS

Rj and Dj, measured on LeCroy LC684 Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software.
*Per **MJSQ** spec (Methodologies for Jitter and Signal Quality specifications)*

Pin Description and Recommended Circuit

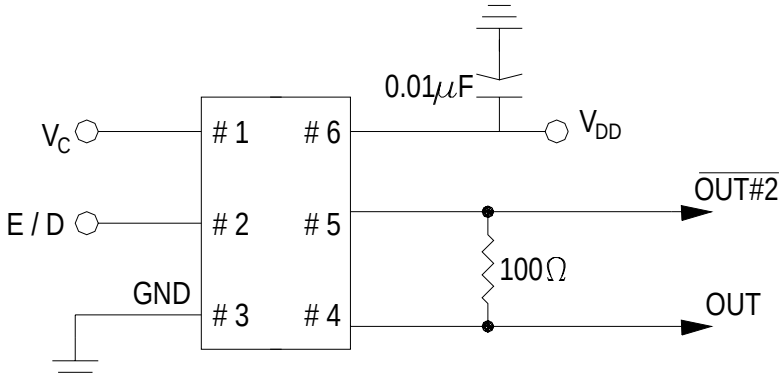
Pin #	Name	Type	Function
1	V_C	Control	Frequency Control by changing voltage
2	E / D [1]	Logic	Enable / Disable Control of Output (0 = Disabled)
3	GND	Ground	Electrical Ground for V_{DD}
4	Output	Output	LVDS Oscillator Output
5	Output 2	Output	Complimentary LVDS Output
6	V_{DD} [2]	Power	Power Supply Source Voltage

NOTES:
[1] Includes pull-up resistor to V_{DD} to provide output when the pin (2)is No Connect.
[2] Installation should include a 0.01µF bypass capacitor placed between V_{DD} (Pin 6)and GND (Pin 3) to minimize power supply line noise.



Terminations as viewed from the Top

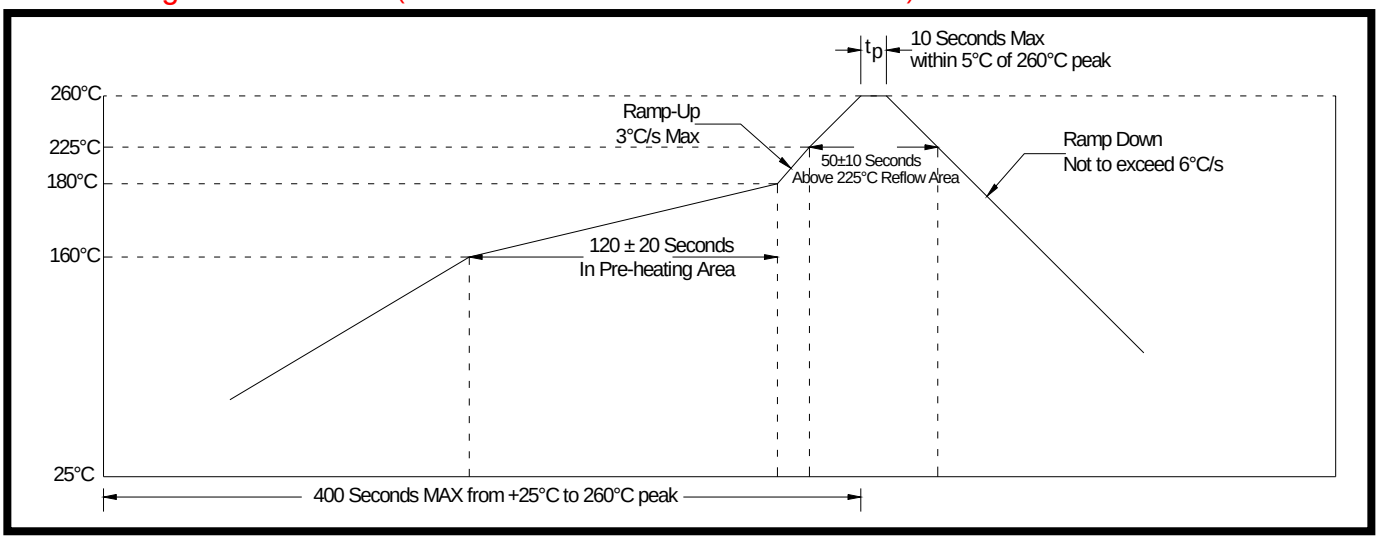
NOTE: LVDS VCXOs are designed to fit on Industry Standard, 6 pad layouts



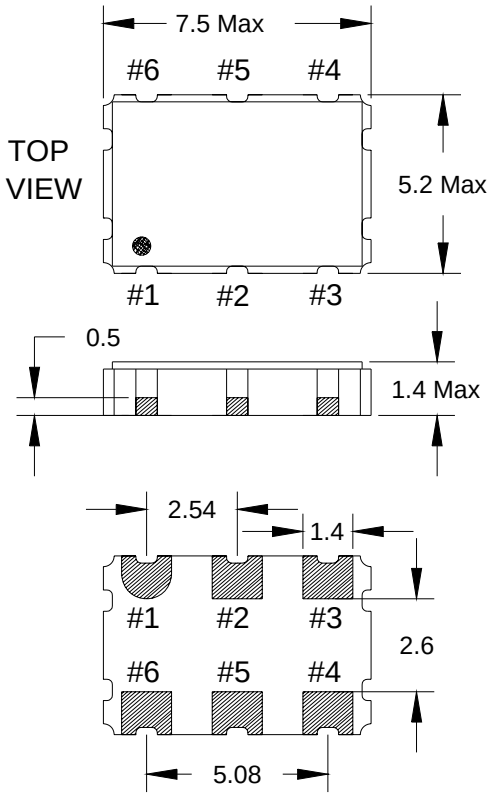
Enable / Disable Control

	Pin # 2 (state)	Output (Pin # 4, Pin # 5)
	OPEN (No Connection)	ACTIVE Output
	"1" Level V_{IH} > 70% V_{DD}	ACTIVE Output
	"0" Level V_{IL} < 30% V_{DD}	High Impedance

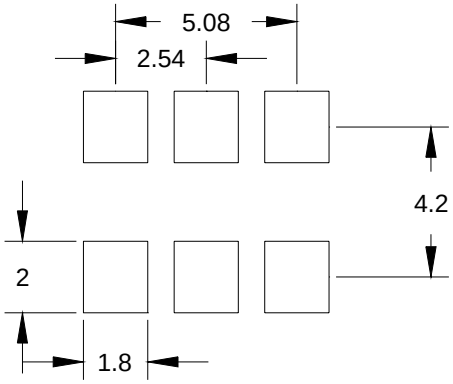
Soldering Reflow Profile (2 times Maximum at 260°C for 10 seconds MAX)



Mechanical Dimensional Drawing & Pad Layout



Recommended
Solder Pad Layout



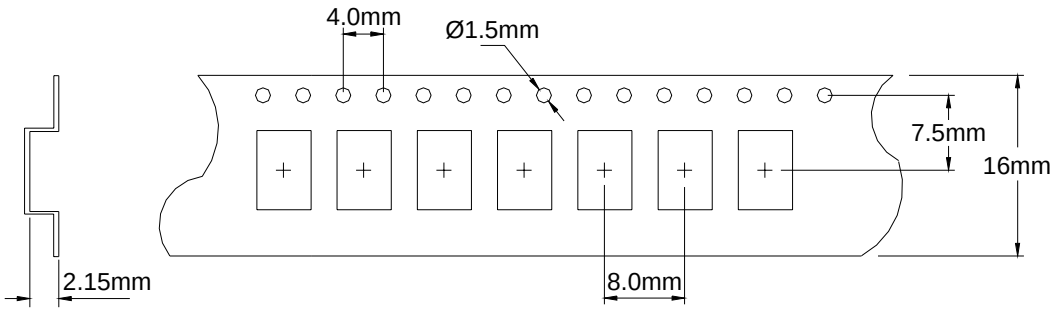
Note: LVDS VCXOs are designed to fit on
Industry standard, 6 pad, layouts.

Pin Connections

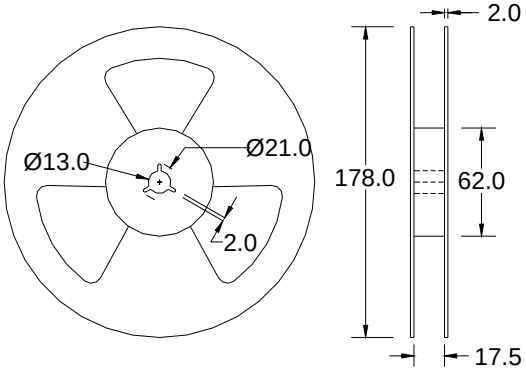
#1) V_C	**#4) Output**
#2) E/D	**#5) Output 2**
#3) GND	**#6) V_{DD}**

Drawing is for reference to critical specifications defined by size measurements.
Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary

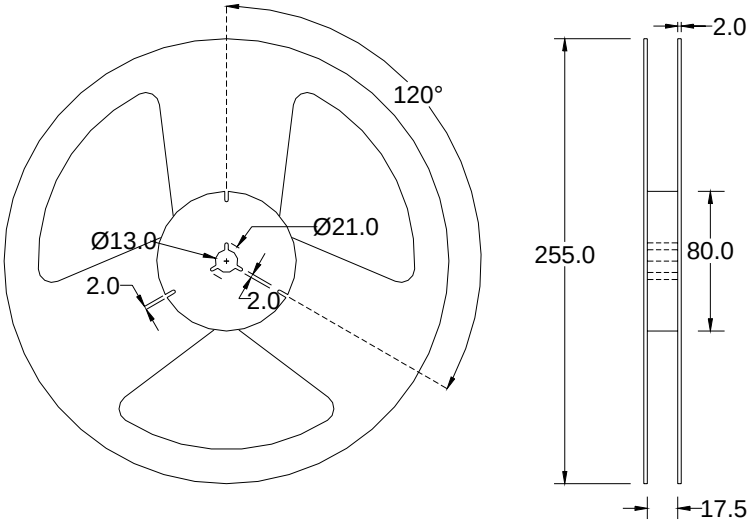
Tape and Reel Dimensions



1k Reel Dimensions in mm



2k Reel Dimensions in mm



Mechanical Testing

Parameter	Test Method
Mechanical Shock	Drop from 75cm to hardwood surface – 3 times
Mechanical Vibration	10~55Hz, 1.5mm amplitude, 1 Minute Sweep 2 Hours each in 3 Directions (X, Y, Z)
High Temperature Burn-in	Under Power @ 125°C for 2000 Hours (results below)
Hermetic Seal	He pressure: 4 ±1 kgf / cm^2 2 Hour soak

2,000 Hour Burn-In

Burn-In Testing – under power 2000 Hours, 125°C